



# The history of revision change for the specification

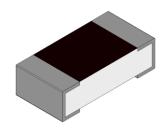
Date	Revision	Changes		
2022/02/22	A0	New approval		
2022/08/05	A1	Update rated power 1/8W		
2022/08/09	A2	Update electrical specifications		
2022/08/17	А3	Update electrical specifications		
2022/11/04	A4	Update pulse table		



# 1/8W & 1/5W, 0402, Anti-Surge, High Power Chip Resistor

### Features / Applications :

- Telecommunication Equipment, Digital Cameras, Watches, Pocket Calculators, Computers, Instruments.
- Excellent surge resistance characteristics
- Halogen Free Epoxy
- RoHS compliant, No RoHS exemption

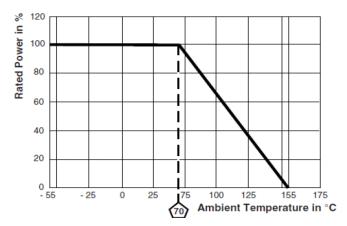


## **Electrical Specifications:**

Characteristics	Feature			
Power Rating*	1/8W,1/5W			
Resistance Values	E-24 & E-96 series			
Resistance Range	1Ω~9.76Ω	10Ω~1ΚΩ		
Temperature Coefficient of Resistance (ppm/°C)	±200	±100		
Resistance Tolerance	±0.5%(D), ±1%(F), ±2%(G), ±5%(J)			
Operation Temperature Range	-55°C ∼ +155°C			

#### \*Note:

For sensor operated at ambient temperature in excess of 70°C, the maximum load shall be derated in accordance with the following curve.

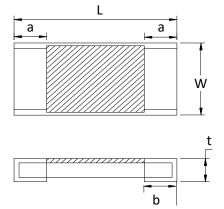


\*\*Note: Resistors shall have a rated DC or AC(rms.) continuous operating voltage corresponding to the power rating, as calculated from the following formula  $V=\sqrt{P\times R}$ . If the voltage obtained exceeds the maximum operating voltage, this maximum operating voltage shall be the rated voltage.



# Outline Drawing:





L	1.00 ± 0.10
W	0.50 ± 0.10
t	0.35 ± 0.10
a	0.20 ± 0.15
b	0.25 ± 0.15

Unit: mm

# Type Designation:

HPFR05S - 🗆 🗆 🗆 - 🗆 TF

(1) (2) (3) (4) (5)

Note:

- (1) Series No.
- (2) Nominal resistance value:

For example -

Three digits of number (E-24 Series)

 $1R1 = 1.1\Omega$ 

 $470 = 47\Omega$ 

Four digits of number (E-96 Series)

 $1R02 = 1.02\Omega$ 

 $49R9 = 49.9\Omega$ 

The "R" shall be used as a decimal point

- (3) Tolerance (%): D=±0.5%, F=±1%, G=±2%, J=±5%
- (4) Power Rating: 2=1/8W, H = 1/5W;
- (5) TF= total Lead free ( without RoHS exemption )

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# Characteristics:

#### Electrical

Item	Specification and Requirement	Test Method	
Temperature	As electrical specifications	Room temperature	
Coefficient of		Room temperature +100°C	
Resistance(ppm/°C)			
Short Time Overload	ΔR: ±(2%+ 0.0005Ω)	(1) Preconditioning	
	Without damage by flashover, spark,	(2) 2.5 * rated voltage for 5seconds	
	arcing, burning or breakdown		
Insulation Resistance	Over 100 M $\Omega$ on Overcoat layer face up	(3) Setup as figure 1	
	Over 1,000 M $\Omega$ on Substrate side face	(4) Test voltage: 100VDC	
	ир	(5) Test time: 60 + 10 / - 0 seconds	
Voltage Proof	△R: ± (2.0%+ 0.0005Ω);	(1) Setup as figure 1	
	Without damage by flashover, spark,	(2) Test voltage: 100VAC(rms.)	
	arcing, burning or breakdown	(3) Test time: 60 + 10 / - 0 seconds	

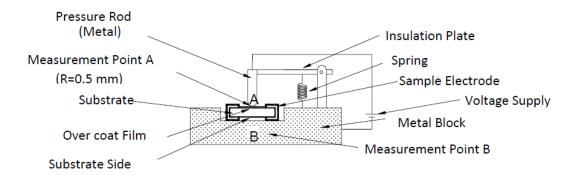


Figure 1 : Measurement Setup





## Mechanical

Item	Specification and Requirement		Test Method
Lead Solderability	ΔR: ±(0.5%+ 0.0005Ω)	(1)	Pretest Requirement: All
,	,		samples shall be subjected to steam
			aging for a period of 8 hours as a
			precondition to testing
		(2)	Lead Solderability 245 ± 5°C for
			2+0/ 0.5 seconds
Reflow Soldering	ΔR: ±(2.0%+ 0.0005Ω);	(1)	Reflow at 260°C for 10 seconds,
_	Without distinct deformation in		3 cycles, and cool down in still
	appearance	(2)	Measuring resistance 1 hours
			after test
Solder Cycles	△R: ±(2.0%+ 0.0005Ω)	(1)	One Reflow cycle;
	Without distinct deformation in	(2)	Followed by cool down; then Hand
	appearance		Soldering:
		(3)	Immersion in molten solder 260°C for
			10 seconds
Vibration Test	ΔR: ±(0.5%+ 0.0005Ω)	(1)	Preconditioning
	Without mechanical damage such as	(2)	Parts are tested at 10Hz to 2kHz at 15g,
	break		20min,12 times per 3 mutually
			perpendicular directions, total 12hours
Mechanical Shock Test	ΔR: ±(0.5%+ 0.0005Ω)	(1)	Preconditioning
	Without mechanical damage such as	(2)	Parts are tested at 50 ± 5G, 11ms,3
	break		shocks per 3 mutually perpendicular
			axes, total 18 shocks
Flexure Strength	ΔR: ±(0.5%+ 0.0005Ω)	Ben	ding value: 2 mm for 10 ± 1 seconds
	Without mechanical damage such as		
	break		
		1	

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## Endurance

Item	Specification and Requirement		Test Method
Thermal cycling	△R: ± (2.0%+ 0.0005Ω)	(1)	Preconditioning
(Air to Air) Test	Without distinct damage in appearance	(2)	Parts are tested at-55 ± 3°C to 125 ± 3°C
			with 30 min dwell time at each
			temperature, 1min max transition time,
			1000 cycles.
		(3)	Measuring resistance 1 hour after test
Temperature Humidity	△R: ±(3.0%+ 0.0005Ω)	(1)	Preconditioning
Test	Without distinct damage in	(2)	Soldering heat
	appearance	(3)	Flexure strength
		(4)	Parts are tested at 1,000 hours at
			60°C ± 2°C, 90-95% RH
Load Life (Endurance)	△R: ± (2.0%+ 0.0005Ω)	(1)	Preconditioning
Test (Power Cycling)	Without distinct damage in	(2)	Parts must be cycled at 70°C at full
	appearance		rated power for 1.5hr power on
			and 0.5hr power off for 1000 hours
Load Life (Endurance)	△R: ± (2.0%+ 0.0005Ω)	(1)	Preconditioning
Test	Without distinct damage in		Parts are tested at a temperature of 125
	appearance		± 2°C and rated power (or maximum
			rated temperature) for 1,000 hours
HAST(Autoclave)	△R: ±(3.0%+ 0.0005Ω)	(1)	Preconditioning
(This test should be	Without distinct	(2)	Soldering heat
performed after	damage in appearance	(3)	Flexure strength;Part are tested for 48
resistance to soldering			hours, 121ºC, 100% R.H., 29.7 psia
heat and flexure			
strength tests)			
Terminal Strength	ΔR: ±(0.5%+ 0.0005Ω)	(1)	Thrust: 5N;
	Without mechanical damage such as	(2)	Times: 30 seconds
	break		

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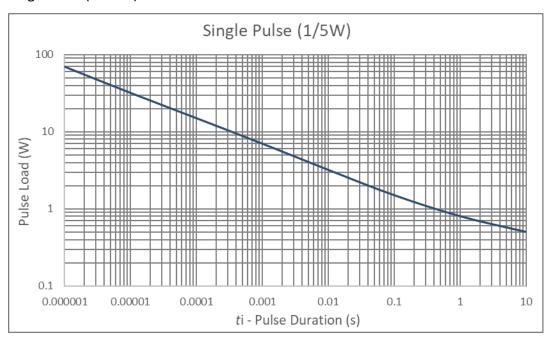
# **Chip Resistor**

Item	Specification and Requirement	Test Method	
Single pulsed	△R: ± (1.0%+ 0.0005Ω)	(1)	Preconditioning
operational life	Without distinct damage in	(2)	Applied for the single pulse and its
	appearance		duration is 1us to 10s
		(3)	The maximum power load is shown in
			Table 1
Continuous pulsed	△R: ±(1.0%+ 0.0005Ω)	(1)	Preconditioning
operational life	Without distinct damage in	(2)	Applied for the continuous pulse,
	appearance		its one cycle: ON time is 1us to 10s
			(ti), OFF time is 10*ti.
		(3)	Test cycles: 1000 cycles
		(4)	The maximum power load is shown
			in Table 2

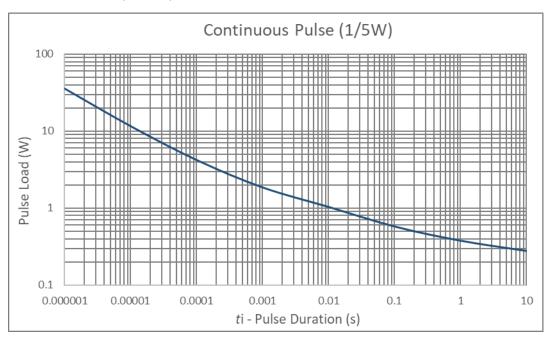
<sup>\*</sup>Preconditioning: 125\*24H  $\,{}^{\backprime}$  85°C / 85%RH\*168H  $\,{}^{\backprime}$  3 reflow cycles



# Single Pulse(Table1)



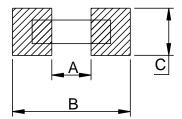
# Continuous Pulse(Table2)



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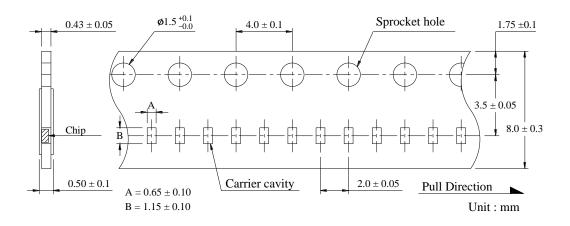
# **Recommend Land Pattern Dimensions:**



А	0.5		
В	1.5		
С	0.4~0.6		

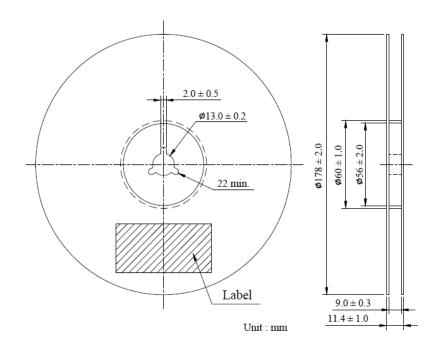
Unit: mm

# TAPE PACKAGING DIMENSIONS:





# **REEL DIMENSIONS:**



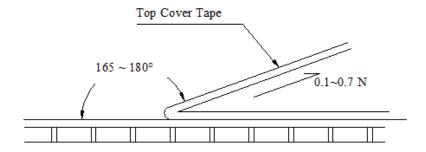
Numbers of Taping: 10,000 pieces/reel

The following items shall be marked on the reel.

- (1) Type designation.
- (2) Quantity
- (3) Manufacturing date code
- (4) Manufacturer's name

Peel force of top cover tape

The peel speed shall be about 300 mm/min. The peel force of top cover tape shall be between 0.1 to 0.7 N.







### Care Note:

#### Care note for storage

- (1) Chip resistor shall be stored in a room where temperature and humidity must be controlled. (temperature 5 to 35°C, humidity 45 to 85% RH) However, a humidity keep it low, as it is possible.
- (2) Chip resistor shall be stored as direct sunshine doesn't hit on it.
- (3) Chip resistor shall be stored with no moisture, dust, a material that will make solderability inferior, and a harmful gas (Chloridation hydrogen, sulfurous acid gas, and sulfuration hydrogen).

#### Care note for operating and handling

- (1) It is necessary to protect the edge and protection coat of resistors from mechanical stress.
- (2) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (3) Resistors shall be used with in rated range shown in specification. Especially, if voltage more than specified value will be loaded to resistor, there is a case it will make damage for machine because of temperature rise depending on generating of heat, and increase resistance value or breaks.
- (4) In case that resistor is loaded a rated voltage, it is necessary to confirms temperature of a resistor and to reduce a load power according to load reduction curve, because a temperature rise of a resistor depends on influence of heat from mounting density and neighboring element.
- (5) Observe Limiting element voltage and maximum overload voltage specified in each specification
- (6) If there is possibility that a large voltage (pulse voltage, shock voltage) charge to resistor, it is necessary that operating condition shall be set up before use.